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AbstractPlus References Full Text: PDF(1536 KB) IEEE JNL |
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AbstractPlus References Full Text: PDF(2446 KB) IEEE JNL |
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Design, Automation and Test in Europe, 2005. Proceedings
2005 Page(s):1176 - 1181
AbstractPlus Full Text: PDF(192 KB) IEEE CNF |
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Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE
Volume 5, 2003 Page(s):2908 - 2912 vol.5
AbstractPlus Full Text: PDF(345 KB) IEEE CNF |
| <input type="checkbox"/> | 6. A 160-Gb/s ATM switching system using an internal speed-up crossbar switch
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Global Telecommunications Conference, 1994. GLOBECOM '94. 'Communications: The Global Bridge', IEEE
28 Nov.-2 Dec. 1994 Page(s):123 - 133 vol.1
AbstractPlus Full Text: PDF(1096 KB) IEEE CNF |
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Defect and Fault Tolerance in VLSI Systems, 2004. DFT 2004. Proceedings. 19th IEEE International Symposium on
10-13 Oct. 2004 Page(s):96 - 104 |

- ☐ **8. Power complexity of multiplexer-based optoelectronic crossbar switches**
Szymanski, T.H.; Honglin Wu; Gourgy, A.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
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[AbstractPlus](#) | Full Text: [PDF\(648 KB\)](#) IEEE JNL

- ☐ **9. Output-queued switch emulation by fabrics with limited memory**
Magill, R.B.; Rohrs, C.E.; Stevenson, R.L.;
Selected Areas in Communications, IEEE Journal on
Volume 21, Issue 4, May 2003 Page(s):606 - 615
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(372 KB\)](#) IEEE JNL

- ☐ **10. Design and Implementation of high-speed symmetric crossbar schedulers**
Hurt, J.; May, A.; Zhu, X.; Lin, B.;
Communications, 1999. ICC '99. 1999 IEEE International Conference on
Volume 3, 6-10 June 1999 Page(s):1478 - 1483 vol.3
[AbstractPlus](#) | Full Text: [PDF\(528 KB\)](#) IEEE CNF

- ☐ **11. Performance evaluation of efficient multipath crossbars**
Mir, N.F.;
Communication Systems, 2002. ICCS 2002. The 8th International Conference on
Volume 1, 25-28 Nov. 2002 Page(s):384 - 388 vol.1
[AbstractPlus](#) | Full Text: [PDF\(379 KB\)](#) IEEE CNF

- ☐ **12. Probabilistic packet delay guarantee in buffered crossbar switch**
Duan, Q.; Daigle, J.N.;
Performance, Computing, and Communications, 2004 IEEE International Conference on
2004 Page(s):253 - 258
[AbstractPlus](#) | Full Text: [PDF\(714 KB\)](#) IEEE CNF

- ☐ **13. Decomposed arbiters for large crossbars with multi-queue input buffers**
Chi, H.-C.; Tamir, Y.;
Computer Design: VLSI in Computers and Processors, 1991. ICCD '91. Proceedings., 1991 IEEE
International Conference on
14-16 Oct. 1991 Page(s):233 - 238
[AbstractPlus](#) | Full Text: [PDF\(492 KB\)](#) IEEE CNF

- ☐ **14. Reliability analysis for one-turn and deflection crossbar architectures and distributed fault recovery scheme**
Oh, M.-S.; Min, P.S.;
Global Telecommunications Conference, 1997. GLOBECOM '97., IEEE
Volume 1, 3-8 Nov. 1997 Page(s):227 - 231 vol.1
[AbstractPlus](#) | Full Text: [PDF\(424 KB\)](#) IEEE CNF

- ☐ **15. Design and analysis of crossbar circuits for molecular nanoelectronics**
Ziegler, M.M.; Stan, M.R.;
Nanotechnology, 2002. IEEE-NANO 2002. Proceedings of the 2002 2nd IEEE Conference on
26-28 Aug. 2002 Page(s):323 - 327
[AbstractPlus](#) | Full Text: [PDF\(462 KB\)](#) IEEE CNF

- ☐ **16. A 2 Gb/s 256*256 CMOS crossbar switch fabric core design using pipelined MUX**
Ting Wu; Chi-Ying Tsui; Hamdi, M.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 2, 26-29 May 2002 Page(s):II-568 - II-571 vol.2
[AbstractPlus](#) | Full Text: [PDF\(476 KB\)](#) IEEE CNF

- ☐ **17. High-performance crossbar design for system-on-chip**
Wijetunga, P.;
System-on-Chip for Real-Time Applications, 2003. Proceedings. The 3rd IEEE International Workshop
on
30 June-2 July 2003 Page(s):138 - 143
[AbstractPlus](#) | Full Text: [PDF\(341 KB\)](#) IEEE CNF

- ☐ **18. Design of an expandable crossbar scheduler based on ISLIP algorithm**
Bo Xu;
Communication Technology Proceedings, 2003. ICCT 2003. International Conference on
Volume 1, 9-11 April 2003 Page(s):540 - 542 vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(273 KB) IEEE CNF
- ☐ **19. A two-dimensional scalable crossbar matrix switch architecture**
Jong Arm Jum; Sung Hyuk Byun; Byung Jun Ahn; Seung Yeob Nam; Dan Keun Sung;
Communications, 2003. ICC '03. IEEE International Conference on
Volume 3, 11-15 May 2003 Page(s):1892 - 1896 vol.3
[AbstractPlus](#) | Full Text: [PDF](#)(414 KB) IEEE CNF
- ☐ **20. An evolution to crossbar switches with virtual output queuing and buffered cross points**
Yoshigoe, K.; Christensen, K.J.;
Network, IEEE
Volume 17, Issue 5, Sept.-Oct. 2003 Page(s):48 - 56
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(749 KB) IEEE JNL
- ☐ **21. Designing and implementing a fast crossbar scheduler**
Gupta, P.; McKeown, N.;
Micro, IEEE
Volume 19, Issue 1, Jan.-Feb. 1999 Page(s):20 - 28
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(120 KB) IEEE JNL
- ☐ **22. Regular sparse crossbar concentrators**
Guo, W.; Oruc, A.Y.;
Computers, IEEE Transactions on
Volume 47, Issue 3, March 1998 Page(s):363 - 368
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(208 KB) IEEE JNL
- ☐ **23. Cost-performance analysis of cascaded crossbar interconnected multiprocessors**
Evequoz, C.;
Computers and Digital Techniques, IEE Proceedings-
Volume 142, Issue 2, March 1995 Page(s):117 - 134
[AbstractPlus](#) | Full Text: [PDF](#)(908 KB) IEE JNL
- ☐ **24. A high-speed and lightweight on-chip crossbar switch scheduler for on-chip interconnection networks**
Lee, K.; Lee, S.-J.; Yoo, H.-J.;
Solid-State Circuits Conference, 2003. ESSCIRC '03. Proceedings of the 29th European
16-18 Sept. 2003 Page(s):453 - 456
[AbstractPlus](#) | Full Text: [PDF](#)(502 KB) IEEE CNF
- ☐ **25. Multiple crossbar network-a switched high speed local network**
Hoebelheinrich, R.; Thomsen, R.;
Local Computer Networks, 1989., Proceedings 14th Conference on
10-12 Oct. 1989 Page(s):285 - 292
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 Abstract Full Text: PDF(660 KB) IEEE CNF</p> |
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 Parallel Processing Symposium, 1992. Proceedings., Sixth International 23-26 March 1992 Page(s):289 - 293
 Abstract Full Text: PDF(336 KB) IEEE CNF</p> |
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 Yan Zhang; Irwin, M.J.;
 Signals, Systems, and Computers, 1999. Conference Record of the Thirty-Third Asilomar Conference on Volume 1, 24-27 Oct. 1999 Page(s):378 - 383 vol.1
 Abstract Full Text: PDF(344 KB) IEEE CNF</p> |
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 Mneimneh, S.S.; Sharma, V.; Kai-Yeung Siu;
 High Performance Switching and Routing, 2001 IEEE Workshop on 29-31 May 2001 Page(s):317 - 323
 Abstract Full Text: PDF(2892 KB) IEEE CNF</p> |
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 Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE Volume 4, 25-29 Nov. 2001 Page(s):2661 - 2665 vol.4
 Abstract Full Text: PDF(194 KB) IEEE CNF</p> |
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 Yavuz Oruc, A.; Huang, H.M.;
 Information Theory, IEEE Transactions on Volume 42, Issue 5, Sept. 1996 Page(s):1466 - 1471
 Abstract Full Text: PDF(892 KB) IEEE JNL</p> |
| <input type="checkbox"/> | <p>32. 16×16 optical-fiber crossbar switch operating at 0.85 μm
 Pedrotti, K.D.; Beccue, S.; Haber, W.J.; Brar, B.P.; Robinson, G.; Kilcoyne, M.K.;
 Lightwave Technology, Journal of Volume 8, Issue 9, Sep 1990 Page(s):1334 - 1342
 Abstract Full Text: PDF(1116 KB) IEEE JNL</p> |
| <input type="checkbox"/> | <p>33. CMOS/nano co-design for crossbar-based molecular electronic systems</p> |

- ☐ **34. Theoretical limitations of a Hopfield network for crossbar switching**
Matsuda, S.;
Neural Networks, IEEE Transactions on
Volume 12, Issue 3, May 2001 Page(s):456 - 462
[Abstract](#) | Full Text: [PDF](#)(140 KB) IEEE JNL

- ☐ **35. A novel and efficient routing architecture for multi-FPGA systems**
Khalid, M.A.S.; Rose, J.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 8, Issue 1, Feb. 2000 Page(s):30 - 39
[Abstract](#) | Full Text: [PDF](#)(172 KB) IEEE JNL

- ☐ **36. Distributed against centralised crossbar function for realising bank-based multiport memories**
Fukae, S.; Inoue, T.; Mattausch, H.J.; Koide, T.; Hironaka, T.;
Electronics Letters
Volume 40, Issue 2, 22 Jan. 2004 Page(s):101 - 103
[Abstract](#) | Full Text: [PDF](#)(251 KB) IEE JNL

- ☐ **37. A massively parallel processing system based on a hyper-crossbar network**
Chin, C.; Lin, W.;
Frontiers of Massively Parallel Computation, 1988. Proceedings., 2nd Symposium on the Frontiers of
10-12 Oct. 1988 Page(s):463 - 466
[Abstract](#) | Full Text: [PDF](#)(284 KB) IEEE CNF

- ☐ **38. The multiple crossbar network interface**
Hedberg, W.F.;
Local Computer Networks, 1989., Proceedings 14th Conference on
10-12 Oct. 1989 Page(s):299 - 306
[Abstract](#) | Full Text: [PDF](#)(464 KB) IEEE CNF

- ☐ **39. Reliable design of multichip nonblocking crossbars**
Ghosh, J.; Varma, A.;
Computer Design: VLSI in Computers and Processors, 1990. ICCD '90. Proceedings., 1990 IEEE
International Conference on
17-19 Sept. 1990 Page(s):70 - 73
[Abstract](#) | Full Text: [PDF](#)(352 KB) IEEE CNF

- ☐ **40. Sonar scene analysis using neuroblonic sound segregation**
Speidel, S.L.;
Neural Networks for Ocean Engineering, 1991., IEEE Conference on
15-17 Aug. 1991 Page(s):77 - 90
[Abstract](#) | Full Text: [PDF](#)(676 KB) IEEE CNF

- ☐ **41. A self routing crossbar switch for multiple channel delta networks**
Ghosh, D.; Daly, J.C.;
Circuits and Systems, 1992., Proceedings of the 35th Midwest Symposium on
9-12 Aug. 1992 Page(s):855 - 858 vol.2
[Abstract](#) | Full Text: [PDF](#)(372 KB) IEEE CNF

- ☐ **42. Providing bandwidth guarantees in an input-buffered crossbar switch**
Stiliadis, D.; Varma, A.;
INFOCOM '95. Fourteenth Annual Joint Conference of the IEEE Computer and Communications
Societies. Bringing Information to People. Proceedings. IEEE
2-6 April 1995 Page(s):960 - 968 vol.3
[Abstract](#) | Full Text: [PDF](#)(964 KB) IEEE CNF

- ☐ **43. Large integrated crossbar switch**
Naik, R.; Walker, D.M.H.;
Wafer Scale Integration, 1995. Proceedings., Seventh Annual IEEE International Conference on

- ☐ **44. Design of a chip set for a parallel computer based on the crossbar interconnection principle**
 Bos, M.L.;
 Circuits and Systems, 1995., Proceedings., Proceedings of the 38th Midwest Symposium on
 Volume 2, 13-16 Aug. 1995 Page(s):752 - 756 vol.2
[Abstract](#) | [Full Text: PDF\(432 KB\)](#) IEEE CNF

- ☐ **45. Crossbar analysis for optimal deadlock recovery router architecture**
 Choi, Y.; Pinkston, T.M.;
 Parallel Processing Symposium, 1997. Proceedings., 11th International
 1-5 April 1997 Page(s):583 - 588
[Abstract](#) | [Full Text: PDF\(620 KB\)](#) IEEE CNF

- ☐ **46. A free space optical crossbar switch using wavelength division multiplexing and vertical-cavity surface-emitting lasers**
 Webb, B.; Louri, A.;
 Massively Parallel Processing, 1998. Proceedings. Fifth International Conference on
 15-17 June 1998 Page(s):50 - 57
[Abstract](#) | [Full Text: PDF\(4 KB\)](#) IEEE CNF

- ☐ **47. A 50 Gb/s 32×32 CMOS crossbar chip using asymmetric serial links**
 Kun-Yung Ken Chang; Shang-Tse Chuang; McKeown, N.; Horowitz, M.;
 VLSI Circuits, 1999. Digest of Technical Papers. 1999 Symposium on
 17-19 June 1999 Page(s):19 - 22
[Abstract](#) | [Full Text: PDF\(564 KB\)](#) IEEE CNF

- ☐ **48. Current-sensing for crossbars**
 Sinha, M.; Burleson, W.;
 ASIC/SOC Conference, 2001. Proceedings. 14th Annual IEEE International
 12-15 Sept. 2001 Page(s):25 - 29
[Abstract](#) | [Full Text: PDF\(312 KB\)](#) IEEE CNF

- ☐ **49. A distributed switch architecture with dynamic load-balancing and parallel input-queued crossbars for terabit switch fabrics**
 Wang, W.; Dong, L.; Wolf, W.;
 INFOCOM 2002. Twenty-First Annual Joint Conference of the IEEE Computer and Communications
 Societies. Proceedings. IEEE
 Volume 1, 23-27 June 2002 Page(s):352 - 361 vol.1
[Abstract](#) | [Full Text: PDF\(333 KB\)](#) IEEE CNF

- ☐ **50. A distributed crossbar switch scheduler for on-chip networks**
 Kangmin Lee; Se-Joong Lee; Hoi-Jun Yoo;
 Custom Integrated Circuits Conference, 2003. Proceedings of the IEEE 2003
 21-24 Sept. 2003 Page(s):671 - 674
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Mhamdi, L.; Hamdi, M.;
High Performance Switching and Routing, 2003, HPSR. Workshop on
24-27 June 2003 Page(s):67 - 72
[AbstractPlus](#) | Full Text: [PDF\(656 KB\)](#) IEEE CNF
- ☐ **52. Comparison of synthesized bus and crossbar interconnection architectures**
Lahtinen, V.; Salminen, E.; Kuusilinn, K.; Hamalainen, T.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on
Volume 5, 25-28 May 2003 Page(s):V-433 - V-436 vol.5
[AbstractPlus](#) | Full Text: [PDF\(339 KB\)](#) IEEE CNF
- ☐ **53. Delay performance analysis for the buffered crossbar switch**
Qiang Duan; Xinghe Li; Linjie Zhang;
Advanced Information Networking and Applications, 2003. AINA 2003. 17th International Conference on
27-29 March 2003 Page(s):750 - 755
[AbstractPlus](#) | Full Text: [PDF\(362 KB\)](#) IEEE CNF
- ☐ **54. Matching output queueing with a multiple input/output-queued switch**
Hyoung-Il Lee; Seouna-Woo Seo;
INFOCOM 2004. Twenty-third Annual Joint Conference of the IEEE Computer and Communications
Societies
Volume 2, 7-11 March 2004 Page(s):1135 - 1146 vol.2
[AbstractPlus](#) | Full Text: [PDF\(799 KB\)](#) IEEE CNF
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Tooley, F.; Suyal, N.; Fritze, A.; Gourlay, J.; Dines, J.; McCarthy, A.; Walker, A.; Bresson, F.;
Microdisplay and Smart Pixel Technologies (Ref. No. 2000/006), IEE Seminar on
17 March 2000 Page(s):6/1 - 6/5
[AbstractPlus](#) | Full Text: [PDF\(444 KB\)](#) IEE CNF
- ☐ **56. 10 Gbit/s/port and 256 ports optical crossbar switch with reduced hardware and enhanced modularity**
Maeno, Y.; Suemura, Y.; Kitamura, S.; Henmi, N.;
Integrated Optics and Optical Fibre Communications, 11th International Conference on, and 23rd
European Conference on Optical Communications (Conf. Publ. No.: 448)
Volume 3, 22-25 Sept. 1997 Page(s):67 - 70 vol.3
[AbstractPlus](#) | Full Text: [PDF\(276 KB\)](#) IEE CNF
- ☐ **57. Transparent optical crossbar switch using liquid-crystal optical light modulator arrays**
Noguchi, K.;
Integrated Optics and Optical Fibre Communications, 11th International Conference on, and 23rd
European Conference on Optical Communications (Conf. Publ. No.: 448)
Volume 4, 22-25 Sept. 1997 Page(s):29 - 32 vol.4

- ☐ **58. Construction of a matrix-matrix crossbar optical interconnect employing diffractive fan-out and fan-in elements**
Walker, A.C.; Taghizadeh, M.R.; Robertson, B.; McKnight, D.J.; Redmond, I.R.; Barrett, C.P.; Blair, P.; White, H.J.; Brownjohn, N.A.; Stace, C.; Proudley, G.M.; Birch, M.J.; Crossland, W.A.;
Holographic Systems, Components and Applications, 1993., Fourth International Conference on
13-15 Sep 1993 Page(s):31 - 34
[AbstractPlus](#) | Full Text: [PDF\(268 KB\)](#) IEE CNF

- ☐ **59. Cost-effective designs of WDM optical interconnects**
Yang, Y.; Wang, J.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 16, Issue 1, Jan 2005 Page(s):51 - 66
[AbstractPlus](#) | Full Text: [PDF\(1120 KB\)](#) IEEE JNL

- ☐ **60. Crossbar demultiplexers for nanoelectronics based on n-hot codes**
Snider, G.S.; Robinett, W.;
Nanotechnology, IEEE Transactions on
Volume 4, Issue 2, March 2005 Page(s):249 - 254
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE JNL

- ☐ **61. A positively self-feedbacked Hopfield neural network architecture for crossbar switching**
Yong Li; Zheng Tang; GuangPu Xia; RongLong Wang;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuits and Systems I:
Fundamental Theory and Applications, IEEE Transactions on]
Volume 52, Issue 1, Jan. 2005 Page(s):200 - 206
[AbstractPlus](#) | Full Text: [PDF\(328 KB\)](#) IEEE JNL

- ☐ **62. Josephson serial-to-parallel decoder**
Feld, D.A.; Van Duzer, T.; Yuh, P.; Kaplan, S.B.;
Applied Superconductivity, IEEE Transactions on
Volume 6, Issue 3, Sept. 1996 Page(s):113 - 124
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1484 KB\)](#) IEEE JNL

- ☐ **63. Embedding classical communication topologies in the scalable OPAM architecture**
Barak, A.; Schenfeld, E.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 7, Issue 9, Sept. 1996 Page(s):979 - 992
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1528 KB\)](#) IEEE JNL

- ☐ **64. Where buses cannot go**
Boxer, A.;
Spectrum, IEEE
Volume 32, Issue 2, Feb. 1995 Page(s):41 - 45
[AbstractPlus](#) | Full Text: [PDF\(488 KB\)](#) IEEE JNL

- ☐ **65. Distributed control schemes for fast arbitration in large crossbar networks**
Ghosh, J.; Varma, A.; Krishnamurthy, N.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 2, Issue 1, March 1994 Page(s):54 - 67
[AbstractPlus](#) | Full Text: [PDF\(1052 KB\)](#) IEEE JNL

- ☐ **66. Reduction of simultaneous-switching noise in large crossbar networks**
Ghosh, J.; Varma, A.;
Circuits and Systems, IEEE Transactions on
Volume 38, Issue 1, Jan. 1991 Page(s):86 - 99
[AbstractPlus](#) | Full Text: [PDF\(1244 KB\)](#) IEEE JNL

- ☐ **67. Neural network architecture for crossbar switch control**
Troudet, T.P.; Walters, S.M.;
Circuits and Systems, IEEE Transactions on
Volume 38, Issue 1, Jan. 1991 Page(s):42 - 56
[AbstractPlus](#) | Full Text: [PDF\(1352 KB\)](#) IEEE JNL

- ☐ **68. Self-routing optical crossbar switch**
Hashimoto, M.; Fukui, M.; Kitayama, K.-I.;
Photonics Technology Letters, IEEE
Volume 2, Issue 7, July 1990 Page(s):522 - 524
[AbstractPlus](#) | Full Text: [PDF\(256 KB\)](#) IEEE JNL
- ☐ **69. Scalable wavelength-converting crossbar switches**
Demir, H.V.; Sabnis, V.A.; Jun-Fei Zheng; Fidaner, O.; Harris, J.S., Jr.; Miller, D.A.B.;
Photonics Technology Letters, IEEE
Volume 16, Issue 10, Oct. 2004 Page(s):2305 - 2307
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(632 KB\)](#) IEEE JNL
- ☐ **70. A VLSI crossbar switch with wrapped wave front arbitration**
Delgado-Frias, J.G.; Ratanpal, G.B.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 50, Issue 1, Jan. 2003 Page(s):135 - 141
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(736 KB\)](#) IEEE JNL
- ☐ **71. Are crossbars really dead? The case for optical multiprocessor interconnect systems**
Nowatzky, A.G.; Prucnal, P.R.;
Computer Architecture, 1995. Proceedings. 22nd Annual International Symposium on
22-24 Jun 1995 Page(s):106 - 115
[AbstractPlus](#) | Full Text: [PDF\(980 KB\)](#) IEEE CNF
- ☐ **72. Experimental photonic multimedia switching system using integrated 8x8 silica-based guided-wave crossbar switch**
Yamaguchi, M.; Matsunaga, T.; Okuno, M.;
Global Telecommunications Conference, 1990, and Exhibition. 'Communications: Connecting the Future', GLOBECOM '90., IEEE
2-5 Dec. 1990 Page(s):1301 - 1305 vol.2
[AbstractPlus](#) | Full Text: [PDF\(412 KB\)](#) IEEE CNF
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Lee, S.G.; Marhic, M.E.;
TENCON '92. Technology Enabling Tomorrow : Computers, Communications and Automation towards the 21st Century. 1992 IEEE Region 10 International Conference.
11-13 Nov. 1992 Page(s):365 - 369 vol.1
[AbstractPlus](#) | Full Text: [PDF\(216 KB\)](#) IEEE CNF
- ☐ **74. Performance analysis of internal buffered crossbar-based ATM switches with LQFS policy**
Fan Yanhe; Wang Jianli; Wang Chuan;
TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering.1993 IEEE Region 10 Conference on
Issue 0, 19-21 Oct. 1993 Page(s):255 - 258 vol.3
[AbstractPlus](#) | Full Text: [PDF\(196 KB\)](#) IEEE CNF
- ☐ **75. Testing crossbar switch interconnection networks**
Sosnowski, J.;
European Test Conference, 1993. Proceedings of ETC 93., Third
19-22 April 1993 Page(s):540 - 542
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Luciani, J.V.; Chen, C.Y.R.;
Global Telecommunications Conference, 1994. GLOBECOM '94. 'Communications: The Global Bridge',
IEEE
Volume 2, 28 Nov.-2 Dec. 1994 Page(s):1065 - 1069 vol.2
[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) IEEE CNF
- ☐ **77. Tight information theoretic density bounds for sparse crossbar concentrators**
Gunduzhan, E.; Oruc, A.Y.;
Information Theory, 1998. Proceedings. 1998 IEEE International Symposium on
16-21 Aug. 1998 Page(s):168
[AbstractPlus](#) | Full Text: [PDF\(88 KB\)](#) IEEE CNF
- ☐ **78. An ADEPT performance model of the Mercury RACEway crossbar interconnection network**
Gang Han; Aylor, J.H.; Klenke, R.H.;
Parallel Interconnects, 1999. (PI '99) Proceedings. The 6th International Conference on
17-19 Oct. 1999 Page(s):83 - 90
[AbstractPlus](#) | Full Text: [PDF\(80 KB\)](#) IEEE CNF
- ☐ **79. CIXB-1: combined input-one-cell-crosspoint buffered switch**
Rojas-Cessa, R.; Oki, E.; Zhigang Jing; Chao, H.J.;
High Performance Switching and Routing, 2001 IEEE Workshop on
29-31 May 2001 Page(s):324 - 329
[AbstractPlus](#) | Full Text: [PDF\(140 KB\)](#) IEEE CNF
- ☐ **80. VLSI design and architecture of a VC-Merge capable crossbar switch on MPLS over ATM**
Young-Chul Kim; Lee, M.M.-O.; Dae-Jin Kim;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):440 - 443
[AbstractPlus](#) | Full Text: [PDF\(449 KB\)](#) IEEE CNF
- ☐ **81. A scalable and high capacity router on multi-dimension crossbar switch principle**
Nalin, T.; Takashi, I.; Morino, H.; Aida, H.; Saito, T.;
Local Computer Networks, 2001. Proceedings. LCN 2001. 26th Annual IEEE Conference on
14-16 Nov. 2001 Page(s):375 - 376
[AbstractPlus](#) | Full Text: [PDF\(275 KB\)](#) IEEE CNF
- ☐ **82. ISKIP: a fair and efficient scheduling algorithm for input-queued crossbar switches**
Wang, W.; Libin Dong; Wolf, W.;
Global Telecommunications Conference, 2002. GLOBECOM '02. IEEE
Volume 3, 17-21 Nov. 2002 Page(s):2287 - 2291 vol.3
[AbstractPlus](#) | Full Text: [PDF\(484 KB\)](#) IEEE CNF

- ☐ **83. Proton single event effects (SEE) testing of the net crossbar switch and network interface card**
 Howard, J.W., Jr.; LaBel, K.A.; Carls, M.A.; Stattel, R.; Rogers, C.E.; Irwin, T.L.;
 Radiation Effects Data Workshop, 2002 IEEE
 15-19 July 2002 Page(s):32 - 40
[AbstractPlus](#) | Full Text: [PDF\(675 KB\)](#) IEEE CNF
- ☐ **84. Throughput of crossbar switches using maximal matching algorithms**
 Benson, K.D.;
 Communications, 2002. ICC 2002. IEEE International Conference on
 Volume 4, 28 April-2 May 2002 Page(s):2373 - 2378 vol.4
[AbstractPlus](#) | Full Text: [PDF\(973 KB\)](#) IEEE CNF
- ☐ **85. On designing of a new simulation platform for crossbar-based scheduling algorithms**
 Peng Wang; Depeng Jin; Lieguang Zeng;
 Communications, 2003. APCC 2003. The 9th Asia-Pacific Conference on
 Volume 3, 21-24 Sept. 2003 Page(s):1190 - 1193 Vol.3
[AbstractPlus](#) | Full Text: [PDF\(460 KB\)](#) IEEE CNF
- ☐ **86. Output queued switch emulation by a one-cell-internally buffered crossbar switch**
 Mhamdi, L.; Hamdi, M.;
 Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE
 Volume 7, 1-5 Dec. 2003 Page(s):3688 - 3693 vol.7
[AbstractPlus](#) | Full Text: [PDF\(340 KB\)](#) IEEE CNF
- ☐ **87. Reducing memory size in buffered crossbars with large internal flow control latency**
 Luijten, R.; Minkenberg, C.; Gusat, M.;
 Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE
 Volume 7, 1-5 Dec. 2003 Page(s):3683 - 3687 vol.7
[AbstractPlus](#) | Full Text: [PDF\(376 KB\)](#) IEEE CNF
- ☐ **88. Stress resistant scheduling algorithms for CIOQ switches**
 Pappu, P.; Turner, J.;
 Network Protocols, 2003. Proceedings. 11th IEEE International Conference on
 4-7 Nov. 2003 Page(s):132 - 143
[AbstractPlus](#) | Full Text: [PDF\(389 KB\)](#) IEEE CNF
- ☐ **89. Weighted fairness in buffered crossbar scheduling**
 Chrysos, N.; Katevenis, M.;
 High Performance Switching and Routing, 2003, HPSR. Workshop on
 24-27 June 2003 Page(s):17 - 22
[AbstractPlus](#) | Full Text: [PDF\(667 KB\)](#) IEEE CNF
- ☐ **90. Universal bounds on buffer size for packetizing fluid policies in input queued, crossbar switches**
 Rosenblum, M.; Goemans, M.X.; Tarokh, V.;
 INFOCOM 2004. Twenty-third Annual Joint Conference of the IEEE Computer and Communications
 Societies
 Volume 2, 7-11 March 2004 Page(s):1126 - 1134 vol.2
[AbstractPlus](#) | Full Text: [PDF\(734 KB\)](#) IEEE CNF
- ☐ **91. SLM-based optical crossbars based on the matrix-matrix principle**
 White, H.; Stace, C.; Birch, M.J.; Walker, A.C.; Taghizadeh, M.R.; Hall, T.J.; Crossland, W.A.;
 Optical Switching, IEE Colloquium on
 3 Jun 1993 Page(s):2/1 - 2/7
[AbstractPlus](#) | Full Text: [PDF\(436 KB\)](#) IEE CNF
- ☐ **92. COBRA: a high performance interconnection for large multicomputers**
 Mackenzie, L.M.; Ould-Khaoua, M.; Sutherland, R.J.; Kelly, T.;
 Medium Grain Distributed Computing, IEE Colloquium on
 26 Mar 1992 Page(s):4/1 - 4/3
[AbstractPlus](#) | Full Text: [PDF\(204 KB\)](#) IEE CNF
- ☐ **93. Researchers work on transistor successor**

- ☐ **94. The throughput of a buffered crossbar switch**
Lin, M.; McKeown, N.;
Communications Letters, IEEE
Volume 9, Issue 5, May 2005 Page(s):465 - 467
[AbstractPlus](#) | Full Text: [PDF](#)(1925 KB) [IEEE JNL](#)

- ☐ **95. The GIGAswitch control processor**
Walsh, R.J.; Ozveren, C.M.;
Network, IEEE
Volume 9, Issue 1, Jan.-Feb. 1995 Page(s):36 - 43
[AbstractPlus](#) | Full Text: [PDF](#)(712 KB) [IEEE JNL](#)

- ☐ **96. The upper bound neural network and a class of consistent labeling problems**
Carlson, R.;
Neural Networks, IEEE Transactions on
Volume 6, Issue 5, Sept. 1995 Page(s):1132 - 1139
[AbstractPlus](#) | Full Text: [PDF](#)(664 KB) [IEEE JNL](#)

- ☐ **97. An analytical model for partially blocking finite-buffered switching networks**
Luciani, J.V.; Chen, C.Y.R.;
Networking, IEEE/ACM Transactions on
Volume 2, Issue 5, Oct. 1994 Page(s):533 - 540
[AbstractPlus](#) | Full Text: [PDF](#)(684 KB) [IEEE JNL](#)

- ☐ **98. Demonstration of an integrated, active 4x4 photonic crossbar [for avionic systems]**
Probst, D.K.; Perrymore, L.G.; Johnson, B.C.; Blackwell, R.J.; Priest, J.A.; Balestra, C.L.;
Photonics Technology Letters, IEEE
Volume 4, Issue 10, Oct. 1992 Page(s):1139 - 1141
[AbstractPlus](#) | Full Text: [PDF](#)(288 KB) [IEEE JNL](#)

- ☐ **99. Neural networks for fast arbitration and switching noise reduction in large crossbars**
Ghosh, J.; Hukkoo, A.; Varma, A.;
Circuits and Systems, IEEE Transactions on
Volume 38, Issue 8, Aug. 1991 Page(s):895 - 904
[AbstractPlus](#) | Full Text: [PDF](#)(832 KB) [IEEE JNL](#)

- ☐ **100. A simple 8x8 optoelectronic crossbar switch**
Forrest, S.R.; Tangonan, G.L.; Jones, V.;
Lightwave Technology, Journal of
Volume 7, Issue 4, April 1989 Page(s):607 - 614
[AbstractPlus](#) | Full Text: [PDF](#)(936 KB) [IEEE JNL](#)

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2003		
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1997		
4	EE	Hsin-Chou Chi, Chih-Tsung Tang : A Deadlock-Free Routing Scheme for Interconnection Networks with Irregular Topologies. ICPADS 1997 : 88-95
1994		
3		Hsin-Chou Chi, Yuval Tamir : Starvation Prevention for Arbiters of Crossbars with Multi-Queue Input Buffers. COMPCON 1994 : 292-297
1993		
2	EE	Yuval Tamir , Hsin-Chou Chi: Symmetric Crossbar Arbiters for VLSI Communication Switches. IEEE Trans. Parallel Distrib. Syst. 4(1): 13-27 (1993)
1991		
1		Hsin-Chou Chi, Yuval Tamir : Decomposed Arbiters for Large Crossbars with Multi-Queue Input Buffers. ICCD 1991 : 233-238

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